	KF1-30 wrt KF2-30	SEL OSS5	<500mΩ	T	
88		<u> </u>	150011122	71mΩ	71mΩ
89	KF1-31wrt KF2-31	SEL_OSS6	<500mΩ	64mΩ	65mΩ
90	KF1-32 wrt KF1-33	SEL_OSS1_Coil13	<500mΩ	5mΩ	5mΩ
91	KF1-34 wrt KF1-35	SEL_OSS2_Coil13	<500mΩ	4mΩ	5mΩ
92	KF1-36 wrt KF1-37	SEL_OSS3_Coil13	<500mΩ	4mΩ	4mΩ
93	KF1-38 wrt KF1-39	SEL_OSS4_Coil13	<500mΩ	4mΩ	4mΩ
94	KF1-40 wrt KF1-41	SEL_OSS5_Coil13	<500mΩ	4mΩ	4mΩ
95	KF1-42 wrt KF1-43	SEL_OSS6_Coil13	<500mΩ	4mΩ	4mΩ
96	KF2-32 wrt KF2-33	SEL_OSS1_Coil2	<500mΩ	5mΩ	5mΩ
97	KF2-34 wrt KF2-35	SEL_OSS2_Coil2	<500mΩ	4mΩ	9mΩ
98	KF2-36 wrt KF2-37	SEL_OSS3_Coil2	<500mΩ	4mΩ	4mΩ
99	KF2-38 wrt KF2-39	SEL_OSS4_Coil2	<500mΩ	4mΩ	4mΩ
100	KF2-40 wrt KF2-41	SEL_OSS5_Coil2	<500mΩ	4mΩ	4mΩ
101	KF2-42 wrt KF2-43	SEL_OSS6_Coil2	<500mΩ	4mΩ	4mΩ
102	All pi	OPEN	>100MΩ	>100MΩ	
103	All Connector m	SHORT	<100mΩ	<100mΩ	

Checklist of SPS CUS_CNTLR

CUS-CNTRL (P & R)

				Test Results			
SI No	Pin Details	Description	Spec	Prime		Redundant	
				ISRC	FSRC	ISRC	FSRC
1	CMDBAR1-37 w.r.to CMDCON2-37	DBA_PY_H_TM2 & DBA_PY_H_TM1	3.920kΩ to 4.080kΩ	4.000kΩ	4.000kΩ	4.000kΩ	4.000kΩ
2	CMDCON2-38 w.r.to CMDCON1-47	DBA_PY_L_TM1 & +15Vin AGND	1.960kΩ to 2.040kΩ	2.000kΩ	2.000kΩ	2.000kΩ	2.000kΩ
3	CMDBAR1-38 w.r.to CMDCON1-47	DBA_PY_L_TM2 &+15Vin AGND	1.960kΩ to 2.040kΩ	2.000kΩ	2.000kΩ	2.000kΩ	2.000kΩ
4	CMDCON2-38 w.r.to CMDBAR1-38	DBA_PY_L_TM1 & DBA_PY_L_TM2	3.920kΩ to 4.080kΩ	4.001kΩ	4.001kΩ	4.000kΩ	4.000kΩ
5	CMDCON2-34 w.r.to CMDCON2-42	VCC & MON5V_H_TM1	1.190kΩ to 1.270kΩ	1.230kΩ	1.230kΩ	1.226kΩ	1.226kΩ
6	CMDCON2-34 w.r.to CMDBAR1-46	VCC & MON5V_H_TM2	1.190kΩ to 1.270kΩ	1.229kΩ	1.229kΩ	1.225kΩ	1.225kΩ
7	CMDCON2-42 w.r.to	MON5V_H_TM1 wrt	1.140kΩ to	1.170kΩ	1.170kΩ	1.168kΩ	1.168kΩ

	1		,		,	,	
8	CMDBAR1-46 w.r.to CMDBAR1-47	MON5V_H_TM2 wrt MON5V_L_TM2	1.140kΩ to 1.180kΩ	1.169kΩ	1.169kΩ	1.168kΩ	1.168kΩ
9	CMDCON1-39 w.r.to CMDCON1-40	5V_ln	<100mΩ	8mΩ	7mΩ	8mΩ	7mΩ
10	CMDCON1-42 w.r.to CMDCON1-43	CND 0	<100mΩ	7mΩ	7mΩ	8mΩ	7mΩ
11	CMDCON1-42 w.r.to CMDBAR1-47	GND & MON5V_L_TM2,	<100mΩ	10mΩ	9mΩ	11mΩ	10mΩ
12	CMDCON1-42 w.r.to CMDCON2-43	MON5V_L_TM1	<100mΩ	10mΩ	21mΩ	11mΩ	21mΩ
13	CMDCON1-45 w.r.to CMDCON1-46	+15Vin	<100mΩ	9mΩ	8mΩ	8mΩ	8mΩ
14	CMDCON1-47 w.r.to CMDCON1-48	AGND	<100mΩ	8mΩ	7mΩ	7mΩ	7mΩ
15	CMDCON1-49 w.r.to CMDCON1-50	-15Vin	<100mΩ	8mΩ	7mΩ	9mΩ	8mΩ
16	CMDBAR1-42 w.r.to CMDBAR1-43	AGND	<100mΩ	4mΩ	4mΩ	4mΩ	4mΩ
17	CMDBAR1-49 w.r.to CMDBAR1-50	EP_SW(P)	<100mΩ	4mΩ	4mΩ	4mΩ	4mΩ
18	CMDCON2-46 w.r.to CMDCON2-47	28V(P)	<100mΩ	8mΩ	29mΩ	7mΩ	29mΩ
19	CMDCON2-49 w.r.to CMDCON2-50	28VGND(P)	<100mΩ	8mΩ	28mΩ	6mΩ	27mΩ
20	CMDBAR2-42 w.r.to CMDBAR2-43	HC_SEQ	<100mΩ	6mΩ	5mΩ	5mΩ	5mΩ
21	CMDBAR2-44 w.r.to CMDBAR2-45	HC_CON	<100mΩ	6mΩ	5mΩ	5mΩ	5mΩ
22	CMDBAR2-46 w.r.to CMDBAR2-47	28V(R)	<100mΩ	4mΩ	4mΩ	4mΩ	4mΩ
23	CMDBAR2-49 w.r.to CMDBAR2-50	28VGND(R)	<100mΩ	4mΩ	4mΩ	4mΩ	4mΩ
24	CMDCON2-49 w.r.to CMDBAR2-49	28VGND(P) & 28VGND(R)&5V GND&AGND	<100mΩ	11mΩ	21mΩ	11mΩ	21mΩ
25	CMDCON2-49 w.r.to CMDCON1-42		<100mΩ	10mΩ	20mΩ	10mΩ	20mΩ
26	CMDCON2-49 w.r.to CMDCON1-47		<100mΩ	37mΩ	46mΩ	37mΩ	48mΩ
27	CMDCON1-33 w.r.to CMDBAR2-33	OSS1	<750 mΩ	257mΩ	256mΩ	260mΩ	259mΩ
28	CMDCON1-34 w.r.to CMDBAR2-34	OSS2	<750 mΩ	529mΩ	528mΩ	532mΩ	530mΩ
29	CMDCON1-35 w.r.to CMDBAR2-35	OSS3	<750 mΩ	533mΩ	532mΩ	536mΩ	533mΩ
30	CMDCON1-36 w.r.to CMDBAR2-36	OSS4	<750 mΩ	506mΩ	504mΩ	507mΩ	506mΩ
31	CMDCON1-37 w.r.to	OSS5	<750 mΩ	582mΩ	580mΩ	584mΩ	582mΩ

32	CMDCON1-38 w.r.to CMDBAR2-38	OSS6	<750 mΩ	567mΩ	565mΩ	571mΩ	569mΩ
33	CMDCON1-39 w.r.to CMDCON1-42	5V_in wrt GND	4.25kΩ to 4.750kΩ	4.485kΩ	4.486kΩ	4.485kΩ	4.485kΩ
34	CMDCON1-45 w.r.to CMDCON1-47	+15Vin wrt AGND	100kΩ to 125kΩ	114.083kΩ	114.644kΩ	117.445kΩ	118.204kΩ
35	CMDCON1-47 w.r.to CMDCON1-49	AGND wrt -15Vin	13.5kΩ to 15.5kΩ	13.952kΩ	13.942kΩ	13.898kΩ	13.889kΩ
36	CMDCON2-46 w.r.to CMDCON2-49	28V_P wrt GND	>100MΩ	>100MΩ	>100MΩ	>100MΩ	>100MΩ
37	CMDBAR2-46 w.r.to CMDBAR2-49	28V_R wrt GND	>100MΩ	>100MΩ	>100MΩ	>100MΩ	>100MΩ
38	CMDCON2-34 w.r.to CMDCON1-42	VCC (after OVP) wrt GND	180Ω to 280Ω	244.050Ω	244.064Ω	232.629Ω	232.660Ω
39	All pin w.r.t Chassis		>100MΩ	>100MΩ	>100MΩ	>100MΩ	>100MΩ
40	All Connector mounting post wrt chassis		<100mΩ	<100mΩ	<100mΩ	<100mΩ	<100mΩ

List of connectors used in the package 1.CMDCON1,CMDCON2=50M 2.CMDBAR1,CMDBAR2=50F